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SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY:: PUTTUR
(AUTONOMOUS)

MCA II Year II Semester Regular Examinations October-2020
SOFTWARE TESTING

Time: 3 hours

Max. Marks: 60

(Answer all Five Units 5 x 12 = 60 Marks)

UNIT-I

1 Describe Implement and Application of Path Testing. 12 M

OR

2 What is meant by integration testing? Goals of Integration Testing? 12M

UNIT-II

3 a Explain concatenated loops with an example? 6M

b Distinguish Control Flow and Transaction flow. 6M

OR

4 Explain Transaction-flow Testing Techniques. 12M

UNIT-III

5 a With a neat diagram, explain the schematic representation of domain testing. 6M

b Explain about Incomplete domain boundaries. 6M

OR

6 Explain various properties related to Ugly-domains. 12M

UNIT-IV

7 a Discuss Path Sums and Path Product. 8M

b Discuss in brief applications of paths. 4M

OR

8 a Write Short Notes on Distributive Laws. 6M

b Write Short Notes on Identity elements. 6M

UNIT-V

9 a Write testers comments about state graphs. 6M

b What are the types of bugs that can cause state graphs? 6M

OR

10 a What are the software implementation issues in state testing? 6M

b Explain about good state and bad state graphs. 6M

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